



C4	D4			R32 TO 20 OHM		DJR
C4	D4		REM D25	CHG D23, REM +SUREF, R00 R32	12/30/98	DJR
B2	C3			Dx ADDD ACROSS C18	10/30/98	DJR
A1	B2			E8D MODIFICATIONS	5/18/95	DRG
0	0			ALPHA	2/27/95	DRG
SCH. REU.	PCB. REU.	ECO#	PAGE (ZONE)	DESCRIPTION	DATE	APPROV.
REVISION STATUS						
PCB DESIGNER DAVE C		DATE 2/27/95				
ENGINEER DAVE C		DATE 2/27/95	DESCRIPTION 0S-0810 CIRCUIT BOARD SCHEMATIC			
CHECKED		DATE	PRODUCT SPART SLOT MEASURE-UPS			
DWG FILE: E810-C3.SCH			APCC PART NAME 0S-0810-C			REV. 3
DATE:			10/30/98		SHEET 1 OF 1	